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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Ruha et al PATENT NO. 7,702,296 B2
SERIAL NO.: 10/005,766 ART UNIT: 2618
FILED: 11/02/2001 EXAMINER: Tran, Pablo N.
TITLE: Multi-Mode I/O Circuitry Supporting Low Interference
 Signaling Schemes For High Speed Digital Interfaces
ATTORNEY DOCKET NO.: 872.0100.U1(US)
Customer No.: 29,683

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate

MAY 21 2010

Attention: Certificate of Correction Branch

of Correction

Request For Certificate of Correction
(Office Mistake)
35 U.S.C. §254 (37 C.F.R. §1.322)

Sir:

This is a request for a Certificate of Correction (MPEP 1480) in regard to the above-identified patent. Attached is a Form PTO-1050. The mistake(s), incurred through the fault of the Patent and Trademark Office, is clearly disclosed by the records of the Office as indicated by the following description

In the claims:


Claim 1, col. 11, line 36 delete "I/O". Please refer to the amendment filed on July 21, 2009.

Claim 5, col. 12, line 19, delete "ICs" and insert -- integrated circuits--. Please refer to the amendment filed on July 21, 2009.

Claim 17, col. 13, line 31, delete "said". Please refer to the amendment filed on July 21, 2009.

The Office is requested to issue a Certificate of Correction.

Respectfully submitted,



John Garrity (Reg. No. 60,470)

5.12.2010

Date

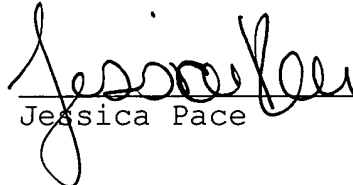
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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail on the date shown below in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

5.12.2010

Date



Jessica Pace

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 7,702,293 B2
APPLICATION NO. : 10/005,766
ISSUE DATE : April 20, 2010
INVENTOR(S) : Ruha et al

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 1, col. 11, line 36 delete "I/O".

Claim 5, col. 12, line 19, delete "ICs" and insert -- integrated circuits--.

Claim 17, col. 13, line 31, delete "said".

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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PATENT NO. 7,702,293 B2

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